

REMARKS

This Amendment is submitted in response to the outstanding Office Action, dated May 17, 2007. The present application was filed on February 26, 2004 with claims 1 through 20. Claims 1, 6-8, 12-14, and 19-20 were cancelled in a prior amendment. Claims 2, 4, 5, 10, 11, 15, 17, and 18 are presently pending in the above-identified patent application prior to entry of the present amendment. Claims 2 and 15 are proposed to be cancelled, without prejudice, herein.

In the Office Action, the Examiner rejected claims 2, 4, 5, 10, 11, 15, 17, and 18 under 35 U.S.C. §102(e) as being anticipated by Salmonsens et al. (United States Patent Publication Number 2004/0054689)

Independent Claims 5, 11 and 18

Independent claims 5, 11, and 18 were rejected under 35 U.S.C. §102(e) as being anticipated by Salmonsens et al.

Independent claims 5, 11, and 18 have now been amended to incorporate the basic limitations of claims 2 and 15. In rejecting claims 2 and 15, the Examiner asserted that Salmonsens teaches a controller wherein said processor is integrated with said controller (citing paragraphs 0049 and 0056).

Contrary to the Examiner's assertion, however, Applicants could find no disclosure or suggestion in Salmonsens that the processor is integrated with the controller. In paragraph 0049, Applicants could find no discussion that has any bearing on this issue. Paragraph 0056 actually *teaches away* from integrating the processor and the controller by stating only that the emulator interface controller 310 may be integrated. There is no mention of the controller 310 being integrated with either processor 314 or 314. In fact, since Salmonsens et al. specify that the controller 310 is integrated, there is a negative inference that the other elements are not integrated.

In addition, each independent claim has been amended to emphasize that the controller is *for use in a peripheral device*. Support for this amendment can be found, for example, in FIG. 2 and the corresponding text. Salmonsens et al. do not disclose or suggest that the controller is part of any peripheral device, including device 202 or the latest embodiment relied on by the Examiner related to the content sink 304.

Thus, Salmonsens et al. do not disclose or suggest a "processor integrated with said controller for controlling communications on a bus," as variously required by each independent claim 5, 11, and 18, as amended. In addition, Salmonsens et al. do not disclose or suggest that the recited controller is "for use in a peripheral device," as variously required by each independent claim 5, 11, and 18, as amended.

Applicants respectfully request the withdrawal of the rejection of independent claims 5, 11 and 18, as amended.

Dependent Claims

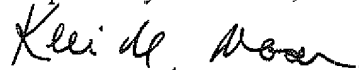
Claims 4, 10 and 17 are dependent on independent claims 5, 11, and 18, respectively, and are therefore patentably distinguished over Salmonsens et al. because of their dependency from amended independent claims 5, 11, and 18 for the reasons set forth above, as well as other elements these claims add in combination to their base claim.

All of the pending claims following entry of the amendments, i.e., claims 4, 5, 10, 11, 17, and 18, are in condition for allowance and such favorable action is earnestly solicited.

If any outstanding issues remain, or if the Examiner has any further suggestions for expediting allowance of this application, the Examiner is invited to contact the undersigned at the telephone number indicated below.

The Examiner's attention to this matter is appreciated.

Respectfully submitted,



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